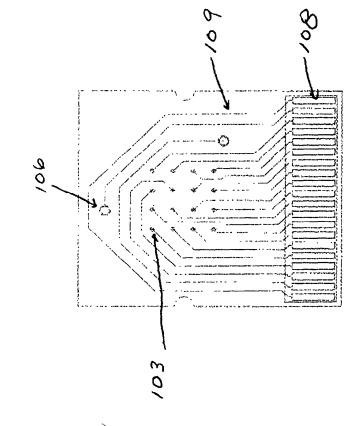
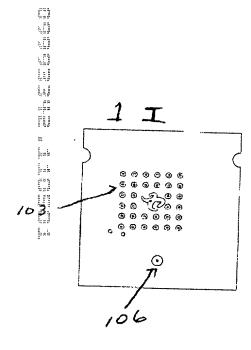
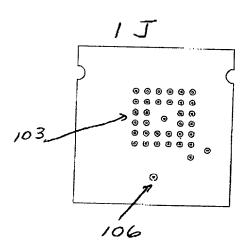


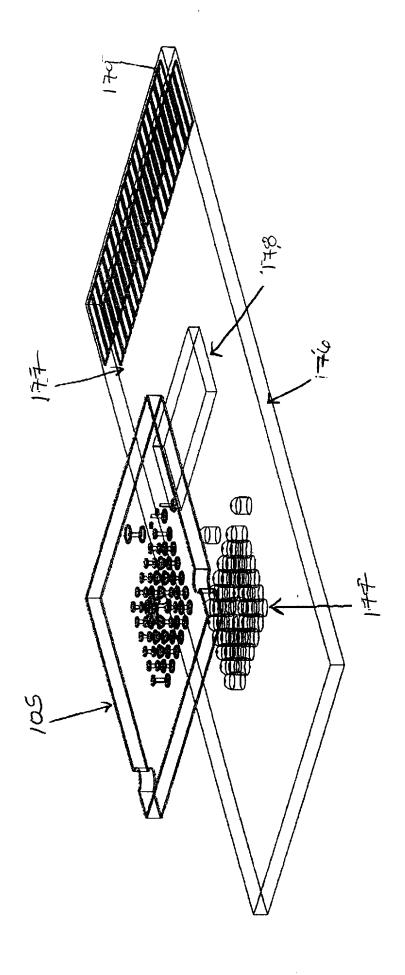
FIGURE 2H

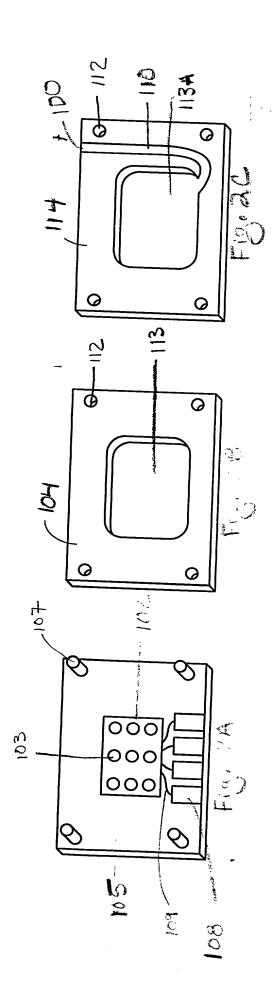


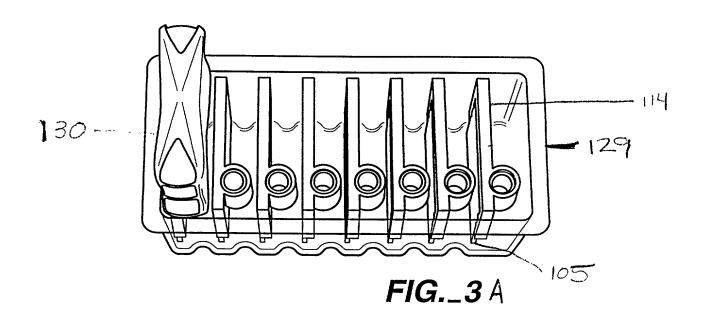


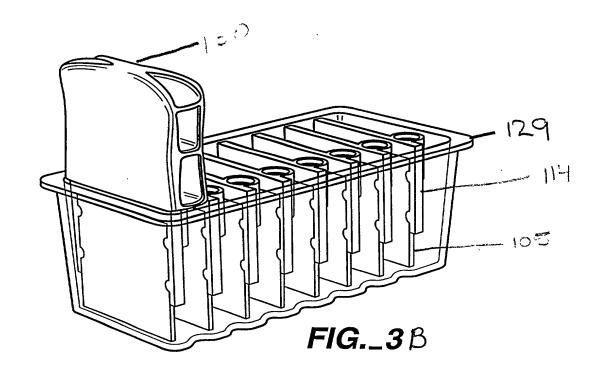


Section with frame to refer the section for the section of the sec











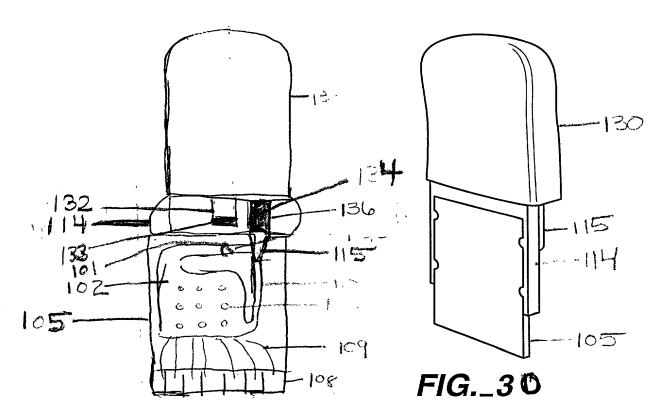


Fig 3C

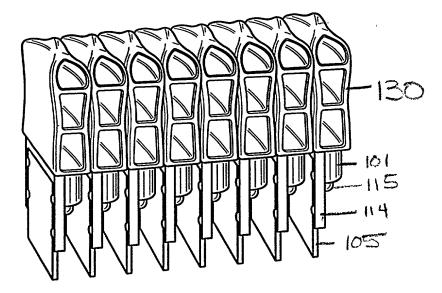
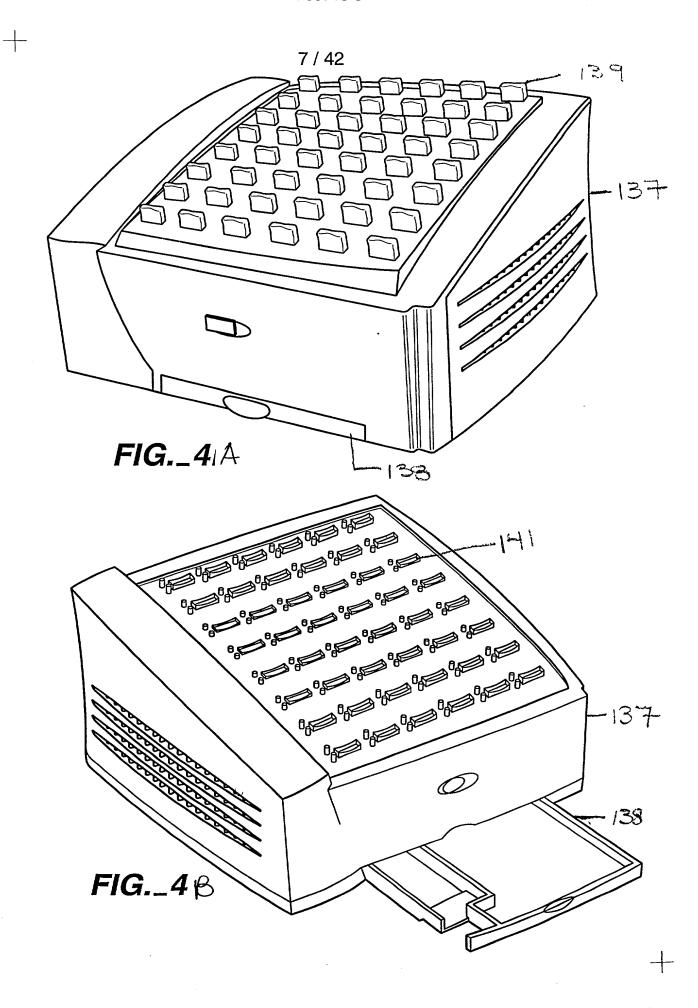
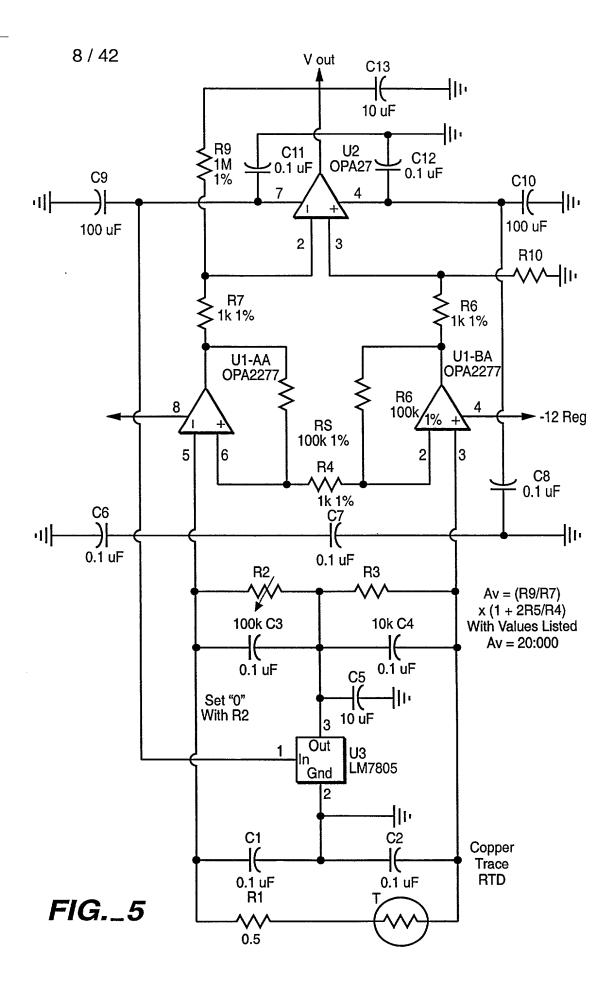


FIG._3 **E**





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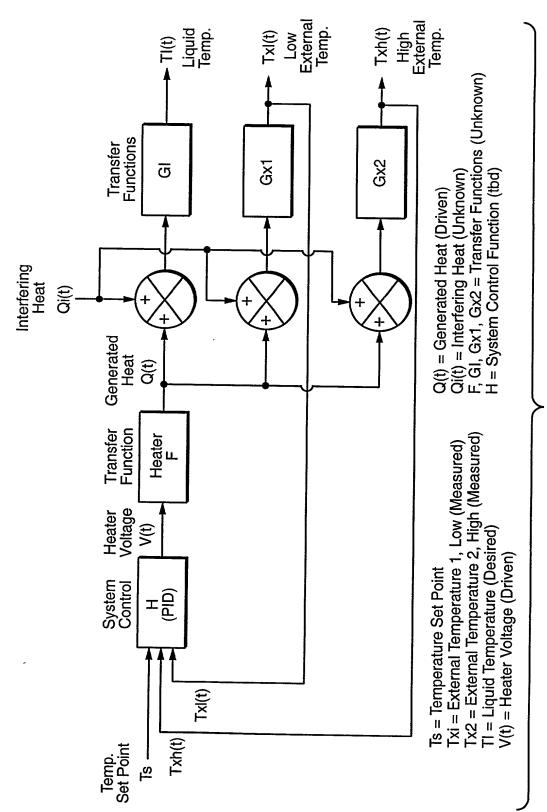
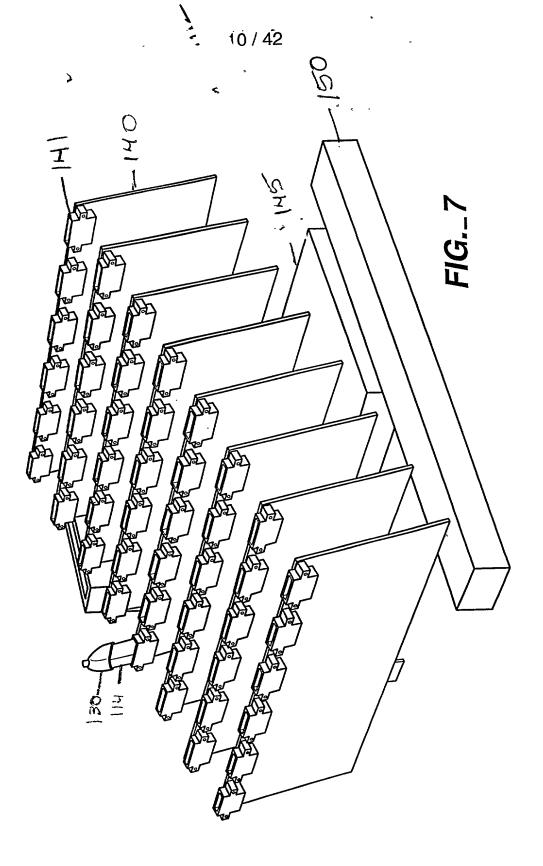
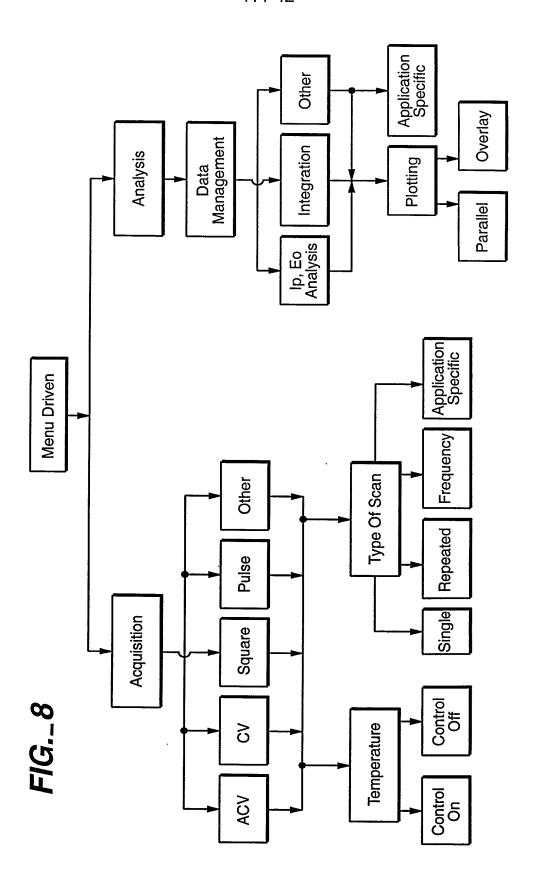


FIG._6

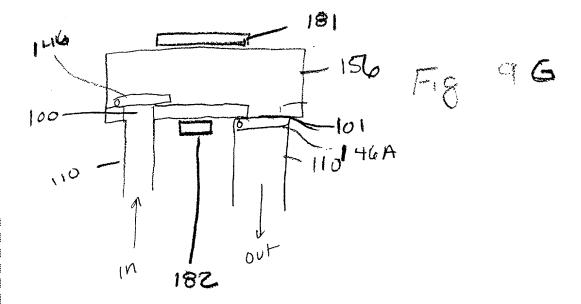


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11 / 42

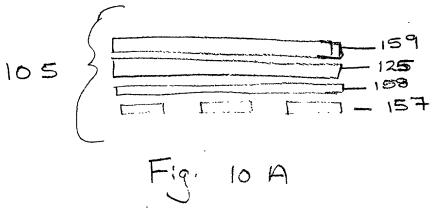


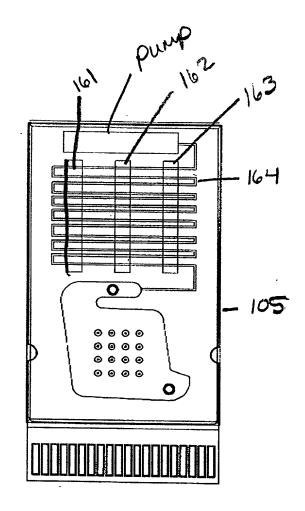
Art art are and Arta Arta Brita in a bud that their dad



1.000

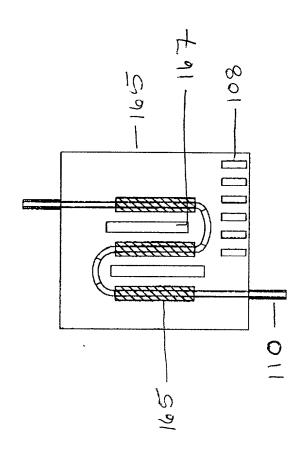
1000000





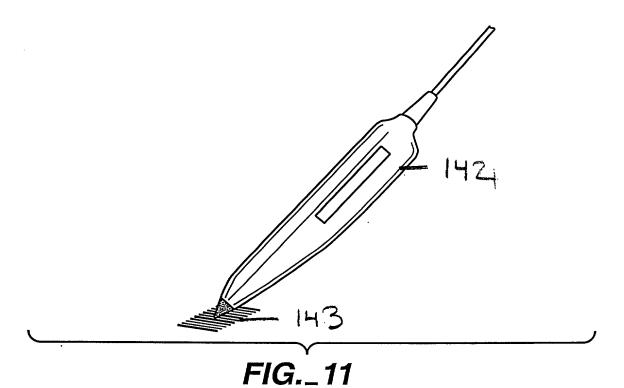
10 B

?



0 0 8

- Bar coded "reference" sheet, stored in tray under unit, with bar coded protocols, bar coded well and slot id's, bar coded commands (e.g. "cancel", "done", etc.)
- Standard bar code wand (preferably with built-in decoder), housed in the tray (hence hidden when not in use)
- Serial (RS-232/485) interface (preferred), or "keyboard wedge"
- Multi-code support (Code 39, Code 128, etc.)
- Bar code on chip carrier (1 code per "8 pack"), identifying test, batch, etc.
 - Peel off labels, with same code as on carrier, with each "8 pack"



+

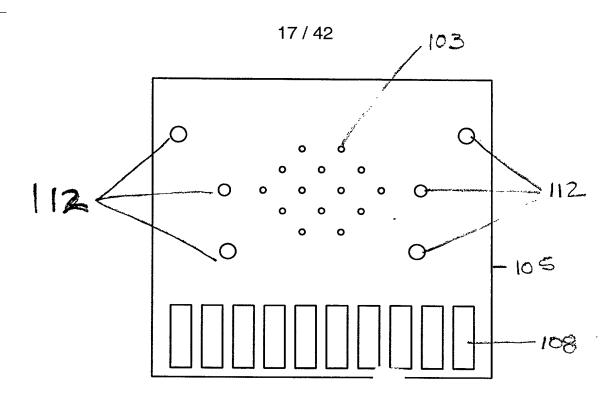
- Bar code usage scenario
 - User fills "8-pack" (all 8, or partially) from a 96 well plate, or from individual sample containers (PCR tubes, vaccutainers, etc.)
 - Pull out tray (with bar code reference sheet) and grab wand
 - Scan "start" code
 - Scan protocol code from sheet (will remain in effect until "done" is scanned)
 - Scan chip code from carrier (will remain in effect until "done" is scanned)
 - For each cartridge, user will
 - □ insert the cartridge in an open slot. Unit senses new chip automatically
 - □ scan the sample ID by either
 - scanning 96 well plate bar code from plate and well code from sheet
 - or scanning unique sample ID from container
 - or scanning "no ID" from reference sheet
 - Scan "done" code. The protocol can' now be started on these cartridges

FIG._12

- Bar code concept benefits
 - No keyboard entry (all-routine setup can be entered via bar coding)
 - All routine entries accomplished while in front of unit (no going back & forth between PC & Hydra)
 - All bar code entries done from small, flat surface in front of unit
 - No need to label each chip or each slot (which would compromise appearance)
 - Uses small unobtrusive bar code wand, hidden when not in use
 - Is flexible with respect to sample container (tube, 96 well plate, etc.), chip usage (by row of 8, or by individual chip), and lab bar coding method

FIG._13





*FIG.*_14 A

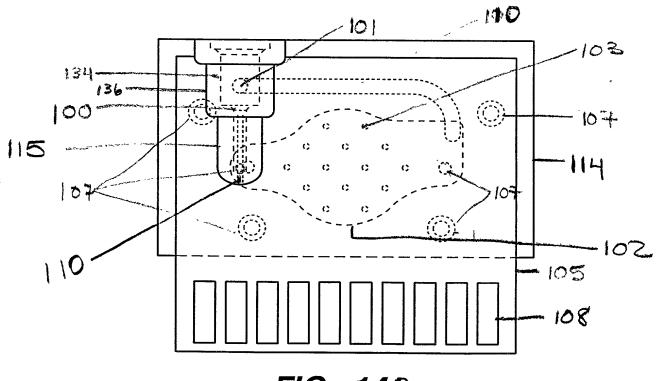


FIG._148

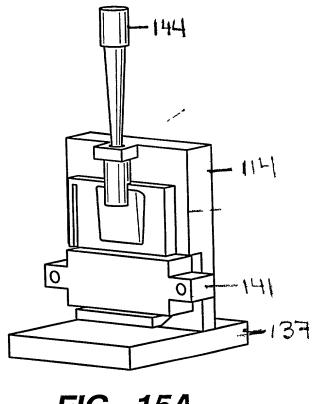


FIG._15A

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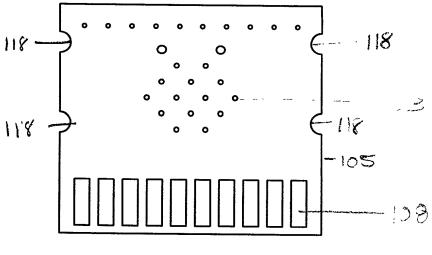


FIG._15 1/2

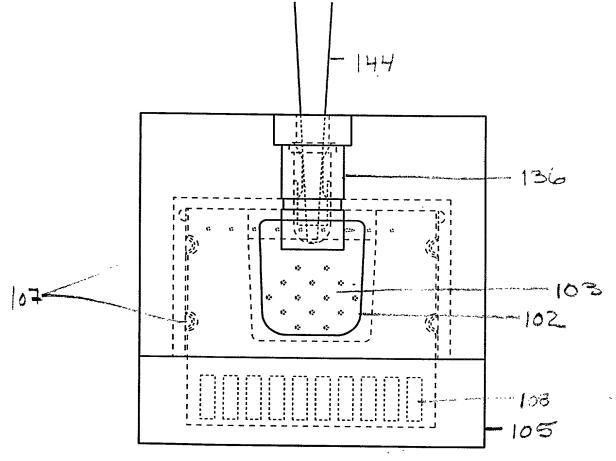
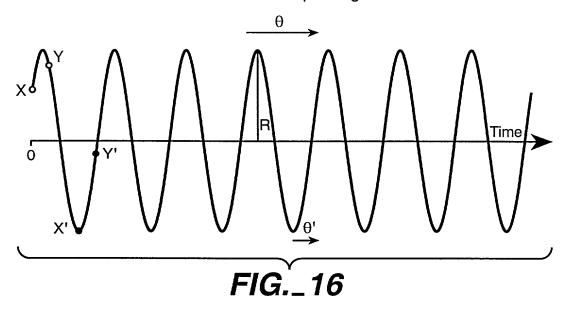
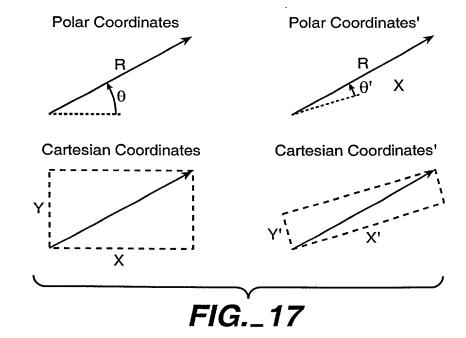


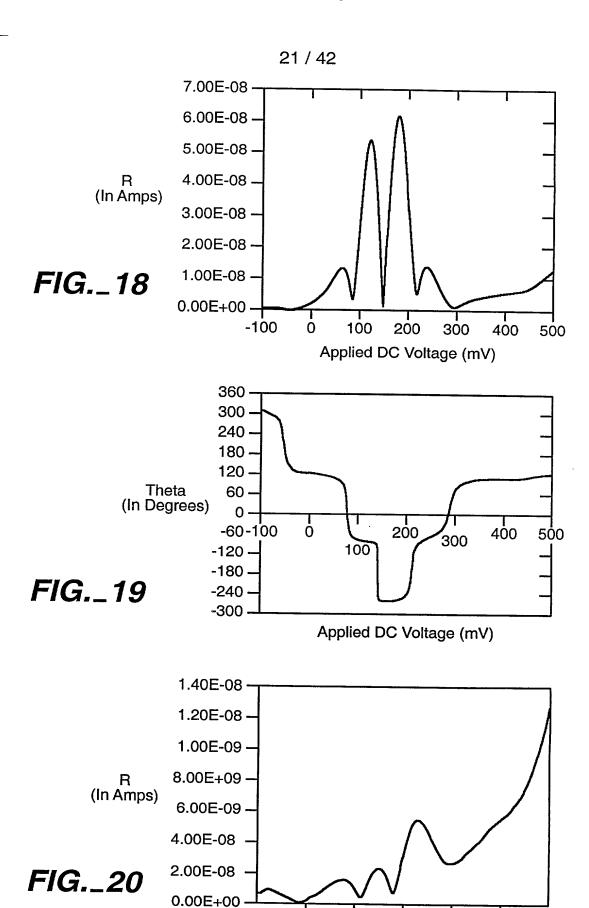
FIG._15 C

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A Sine Wave And Its Corresponding Vector Notation







-100

-|-

400

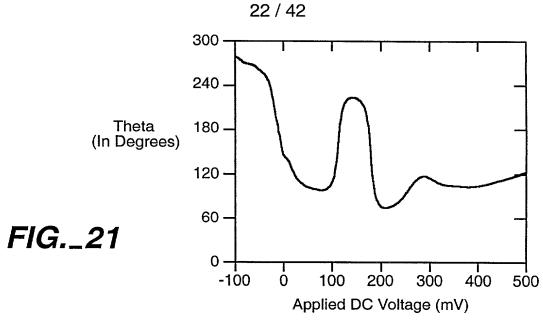
500

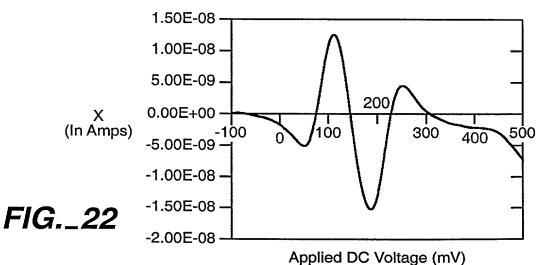
200

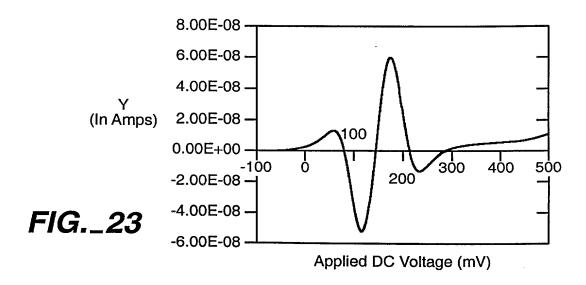
Applied DC Voltage (mV)

100

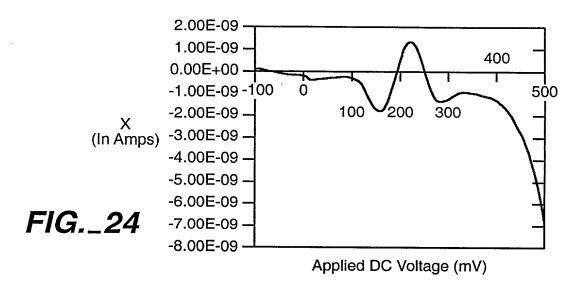
300

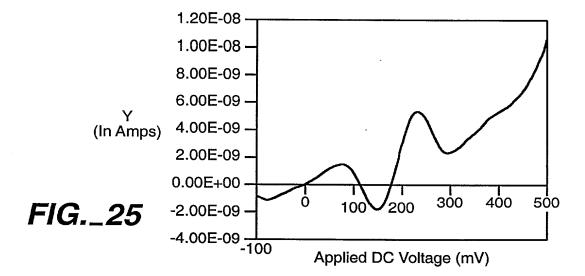






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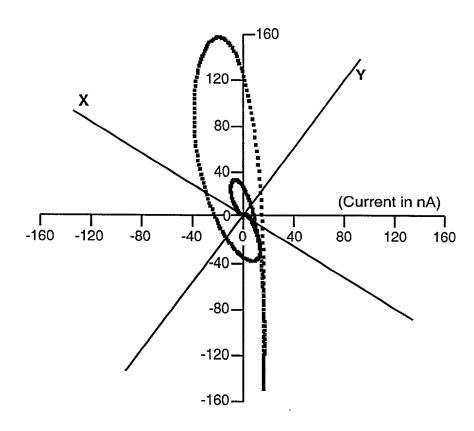


FIG._26

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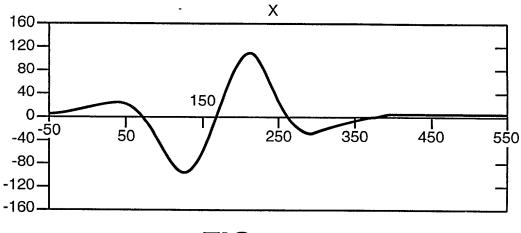


FIG._27

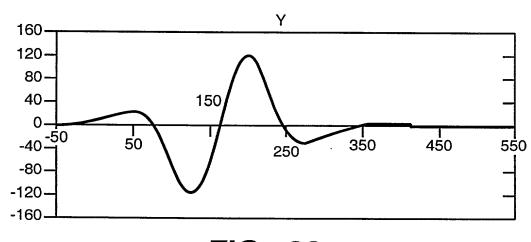


FIG._28

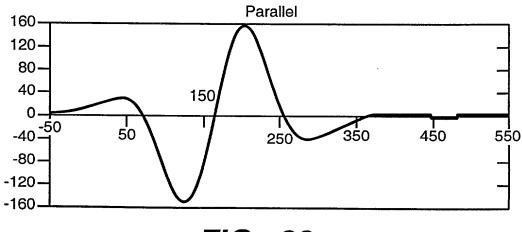
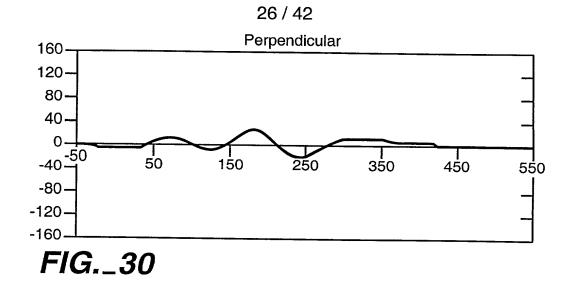
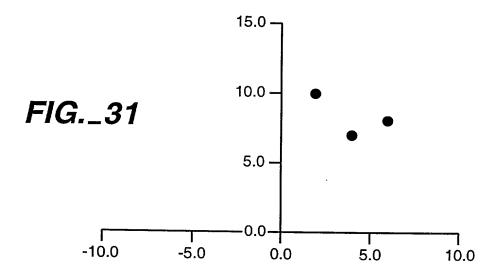
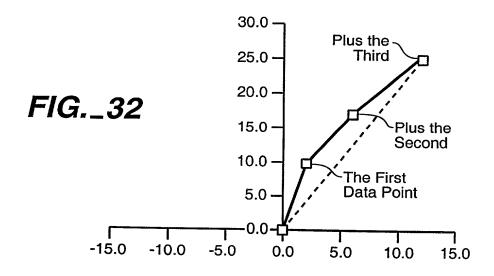


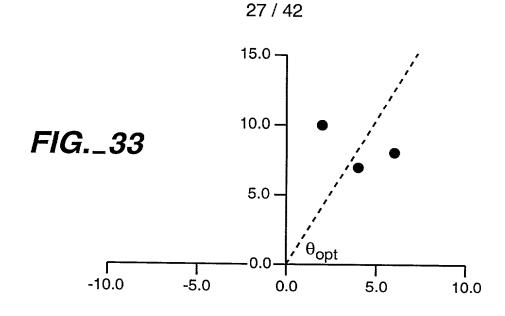
FIG._29

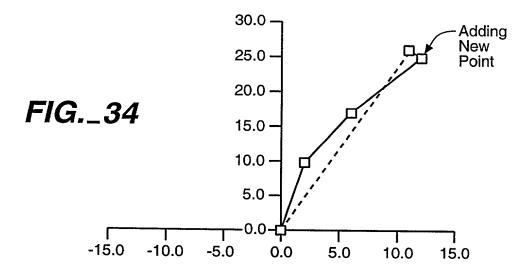
-

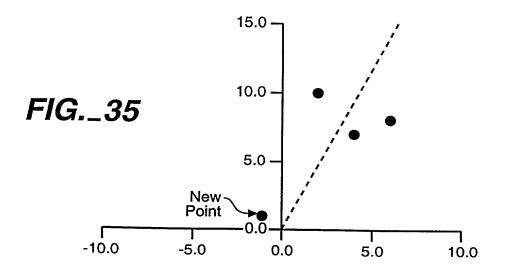


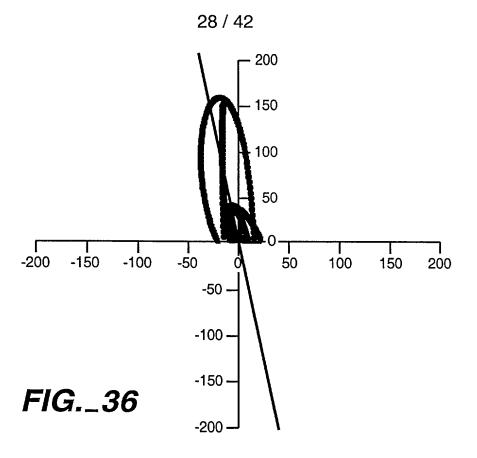


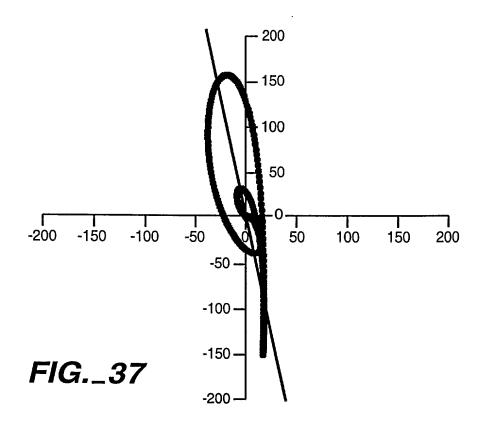


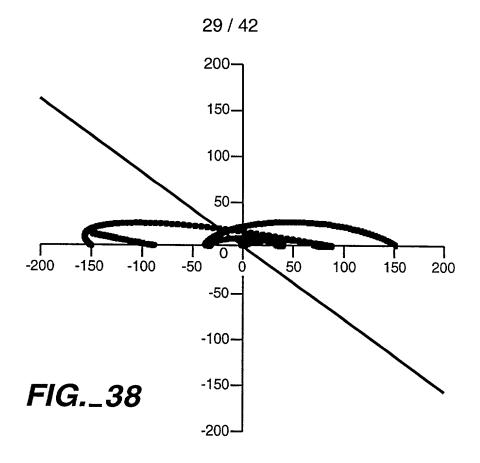


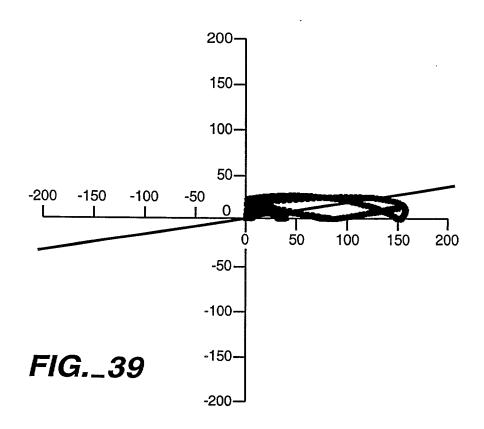


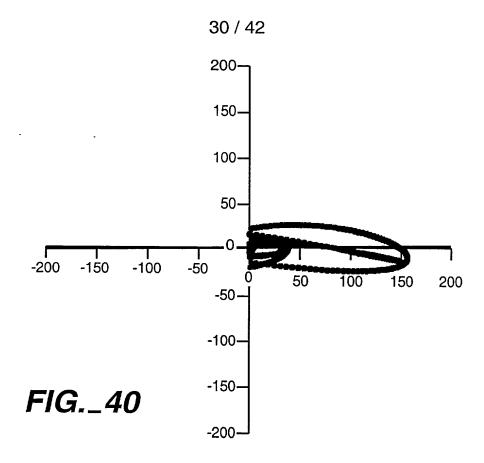


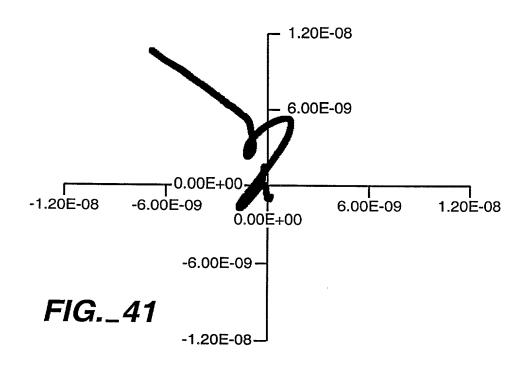


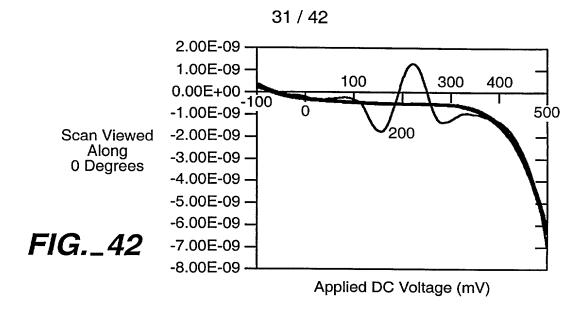


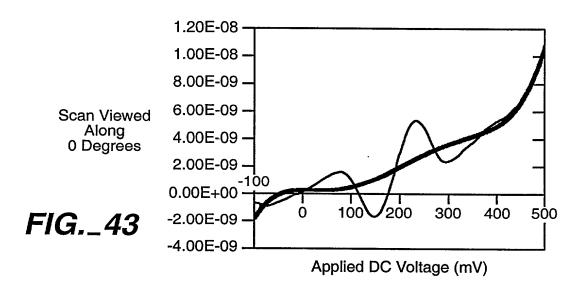


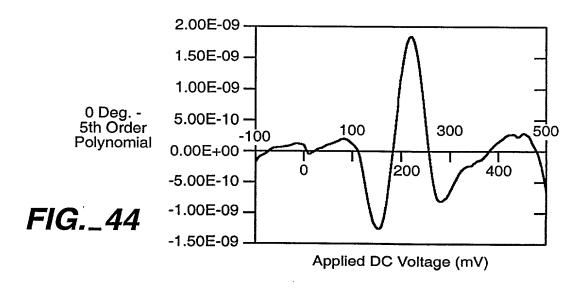












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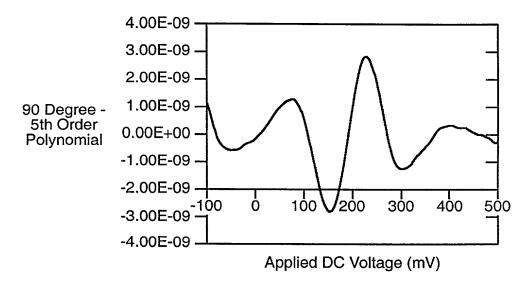


FIG._45

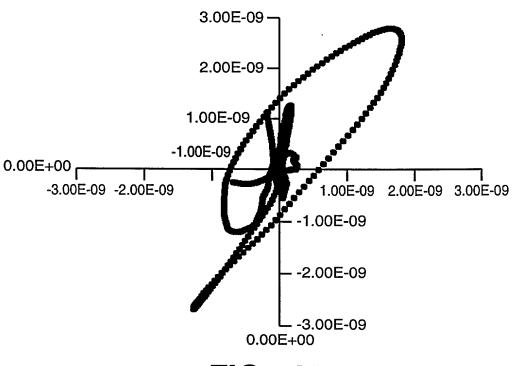


FIG._46

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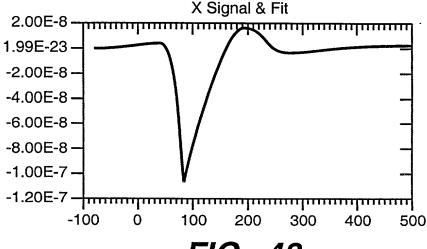
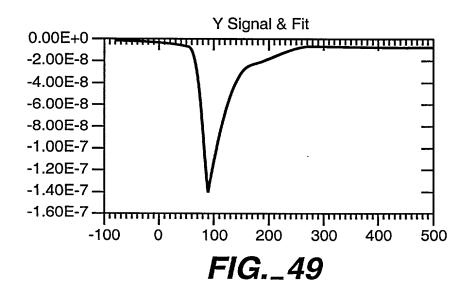


FIG._48



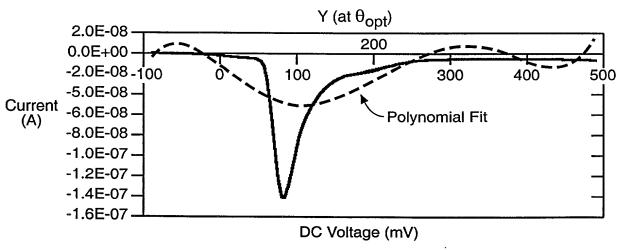
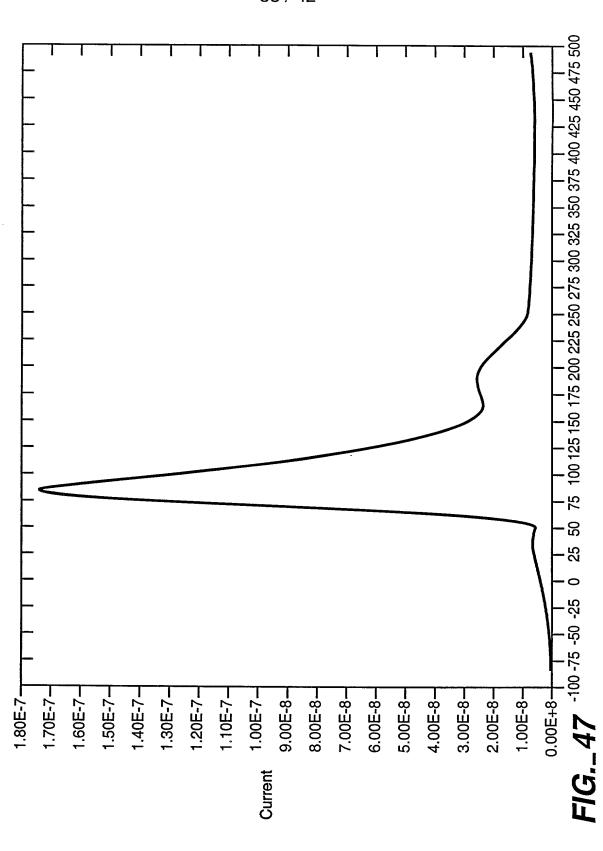
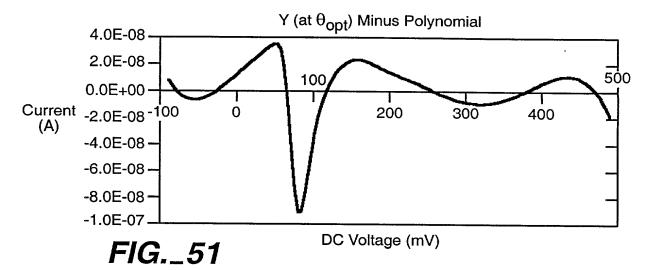


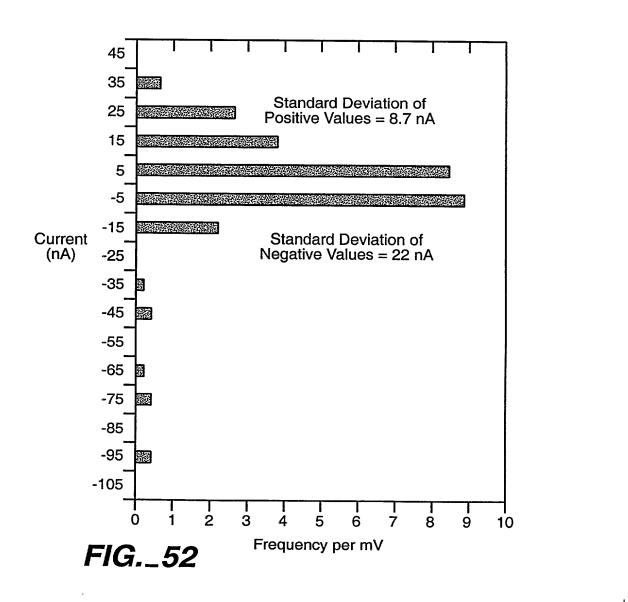
FIG._50



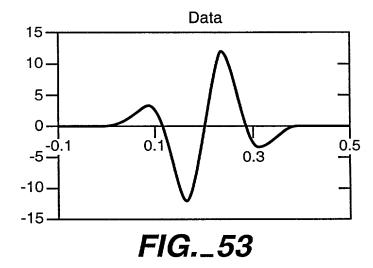


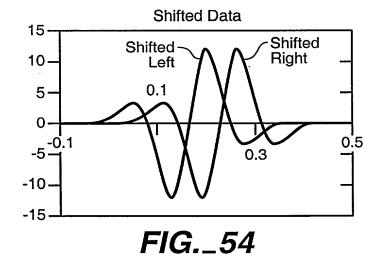






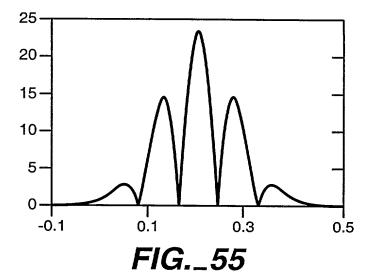
36 / 42





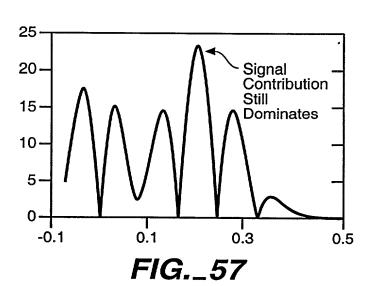
+

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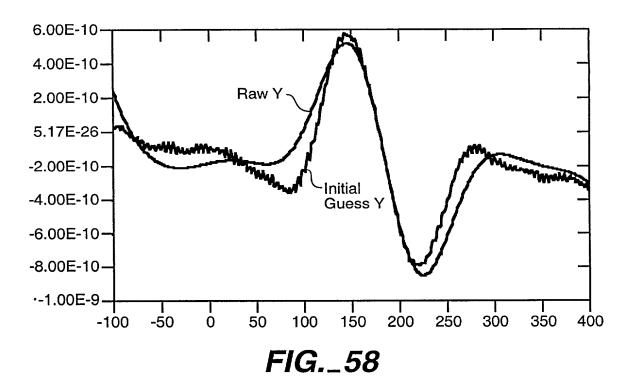
20 15— 10— 5— 0—-5-0.1 0.1 0.3 0.5 -10— -15

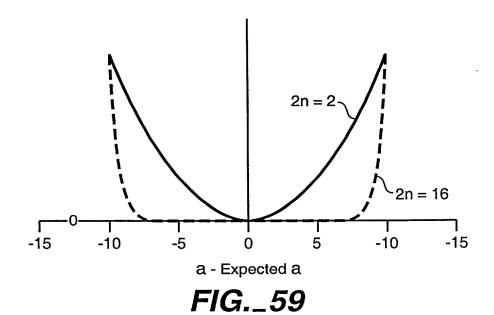
FIG._56



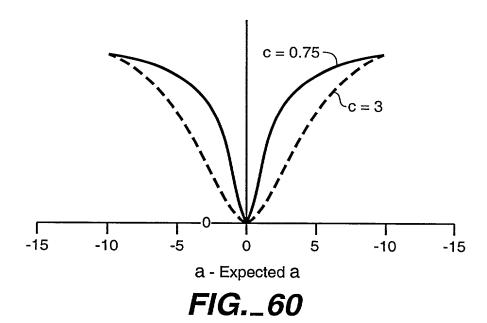
+

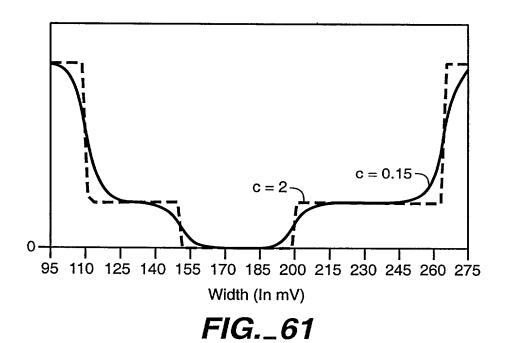
38 / 42





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+



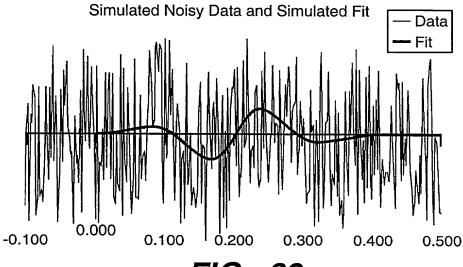
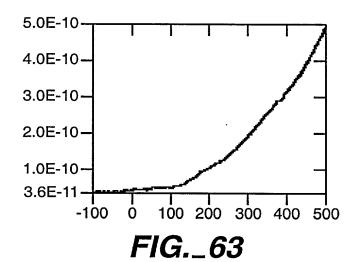
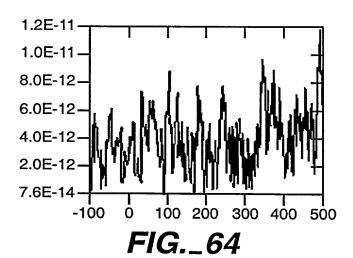


FIG._62





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-100

-50

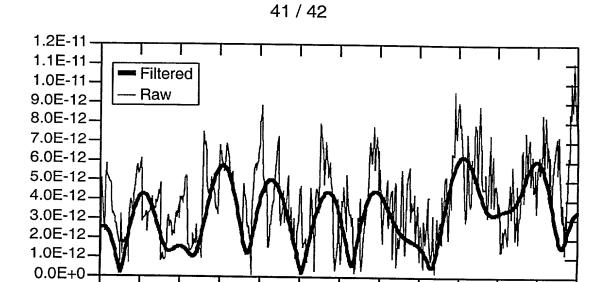


FIG._65

150 200 250

300

350

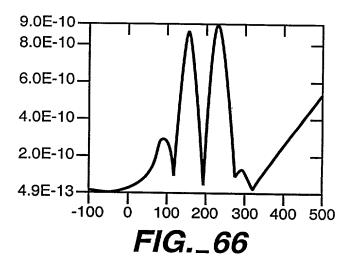
400 450

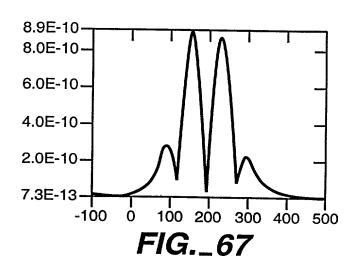
500

50

100

0

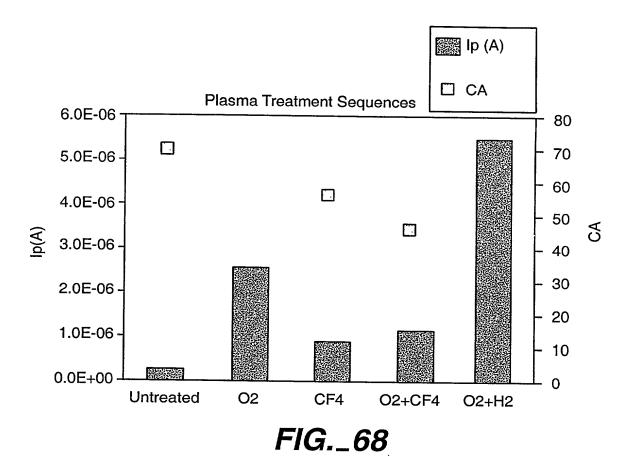




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And the the self-

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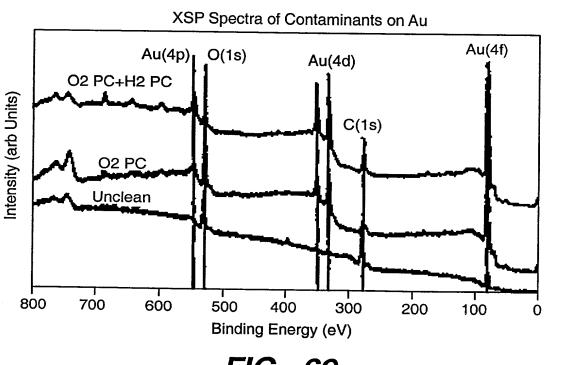
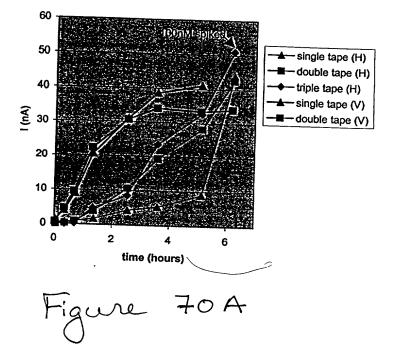


FIG._69



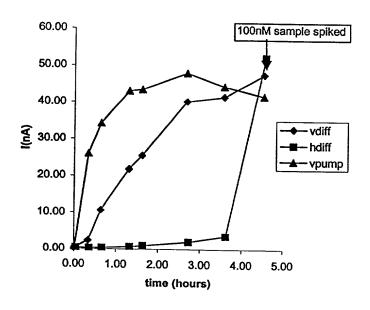


Fig. 70B

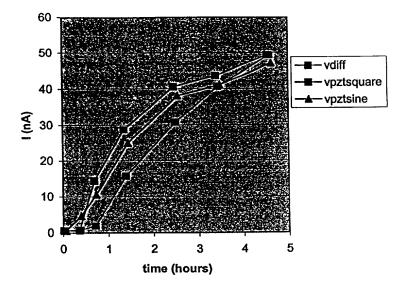
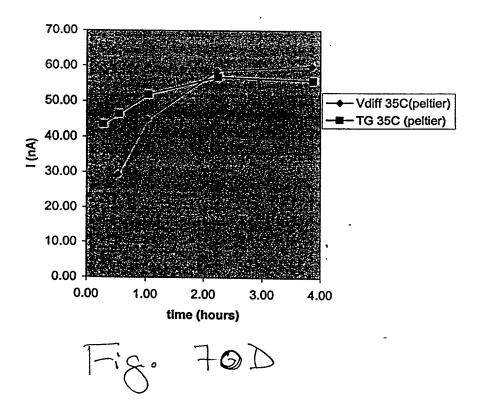
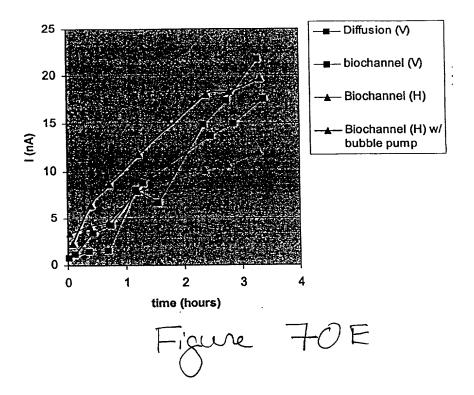
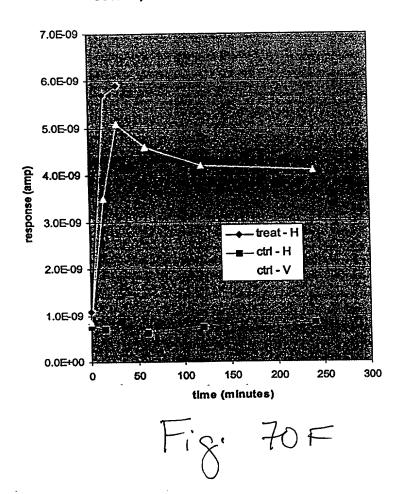


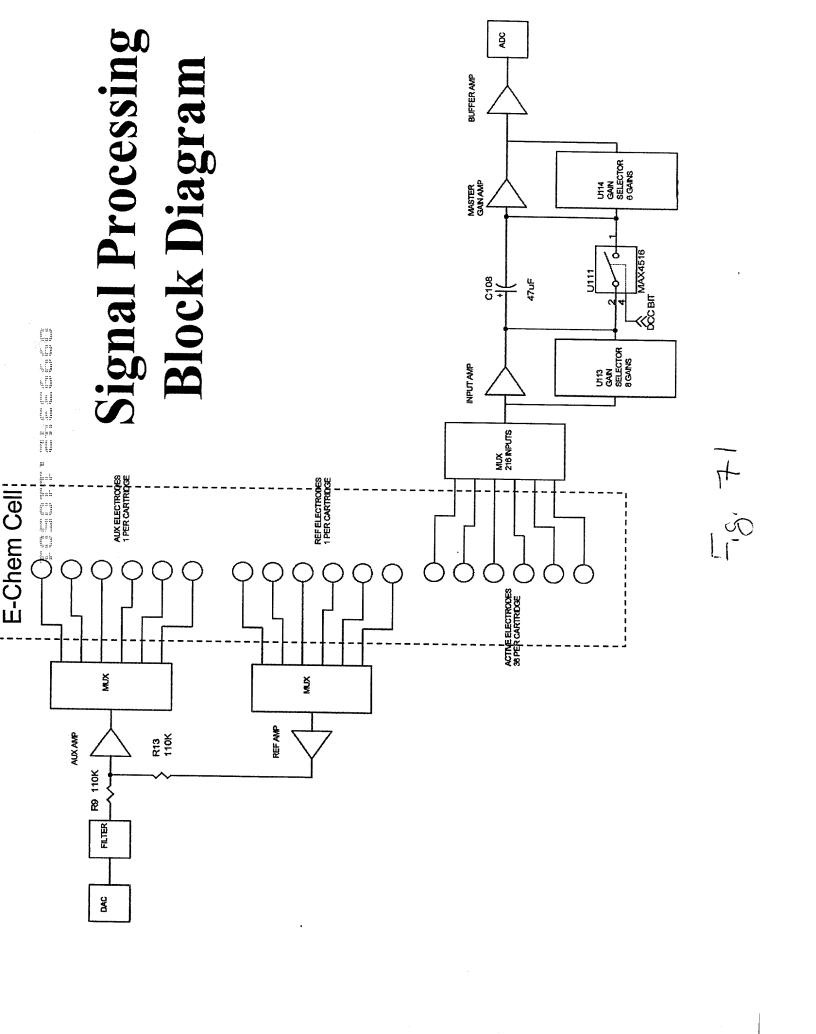
Fig. 70 C





Covaris, Inc. based acoustic mixing

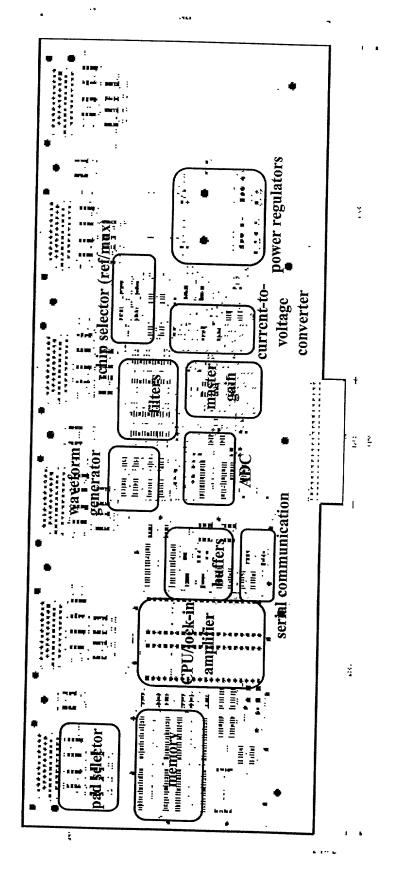




2 SLOT 2 LED DISPLAY RED GREEN 2 SLOT 3 LED DISPLAY RED CREEN SLOT 1 LED DISPLAY SLOT 4 LED DISPLAY SLOT 5 LED DISPLAY 2 SLOT & LED DISPLAY RED GREEN Thermal Control Section British and the Control of 2 HEATSINK TEMPERATURE SENSOR LED DRIVERS(12) (CONSTANT CURRENT) HEATSINK BLOWER PEL TIER THERMAL BLOCK PEL TIER THERNAL BLOCK PELTIER THERMAL BLOCK PEL TIER THERMAL BLOCK PELTIER THERMAL BLOCK PEL TIER THERMAL BLOCK WANGE A (Altre) (AMP) ORIVER (AMP) ANP) AMP) AMP) LED LOGIC (ON/OFF/FLASH) DAC DAC DAC DAC DAC DAC DAC HEATSINK TEMPERATURE FEEDBACK SLOT 1-6 TEMPERATURE SENSOR FEEDBACK MICROPROCESSOR SERIAL COMMUNICATIONS

72+ 81

Signal Processing Board Layout



F18 73